CLAIMS ALLOWED

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	09/986008		•	CONT/PRIOR D F			CLASS 029	SUBCLASS ART UNIT 3729		•	EXAMINER Chora		
APPLICANTS	Takahiro Iijima Akio Rokugawa Tomohiro Nomura Toshinori Koyama							, 					
TITLE	Process for producing multilayer circuit board												
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ORIGIN	AL	CROSS REFERENCE(S)							
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J. J	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent subsequent to(date)		ļ.,		NOTICE OF ALLOWANCE MAILED		
has been disclaimed.	(Assistant	Examiner)	(Date)			
The term of this patent shall not extend beyond the expiration date						
of U.S Patent. No.				ISSUE FEE		
				Amount Due	Date Paid	
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uno patern have been disclaimed.	(Legal Instrume	nts Examiner)	(Date)			
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DRAWINGS

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